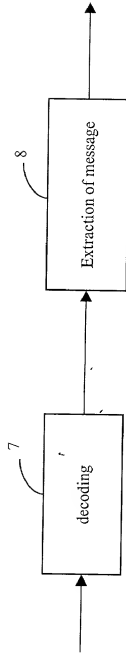
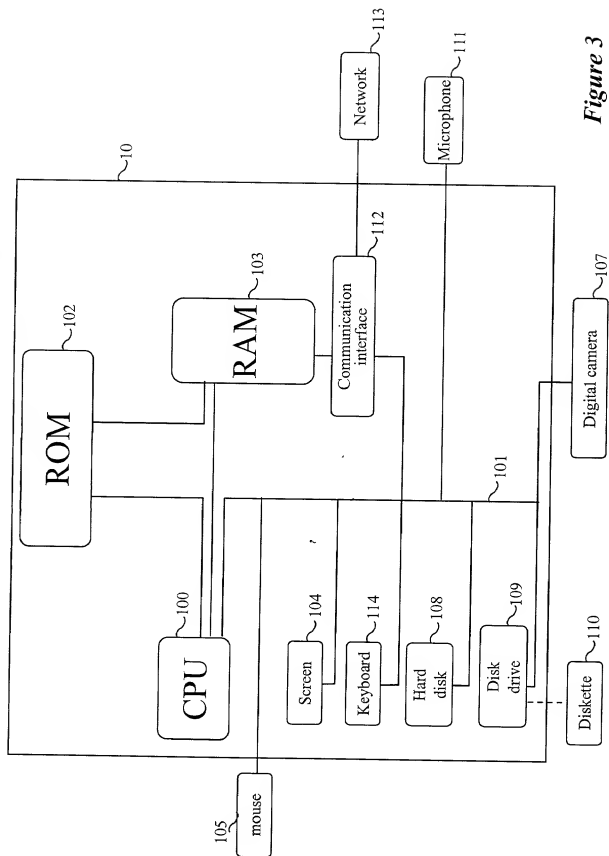
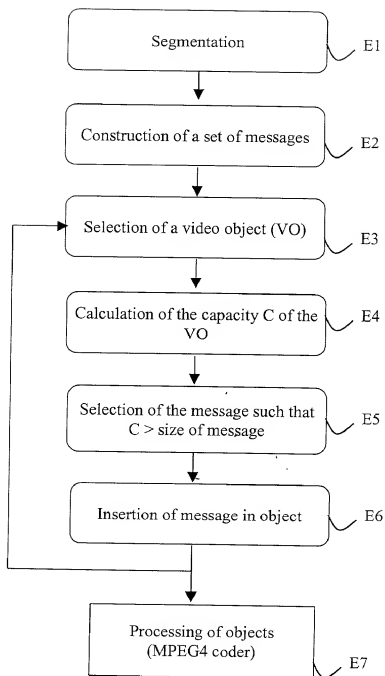
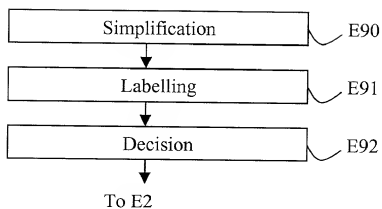
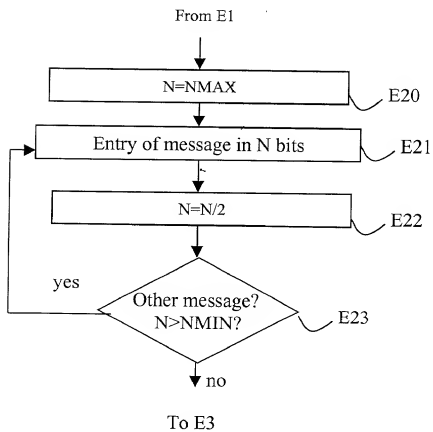


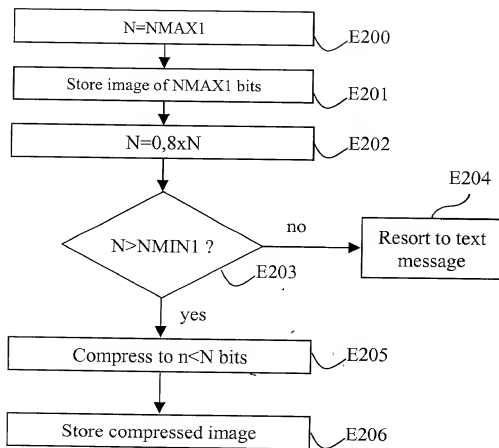
Figure 1

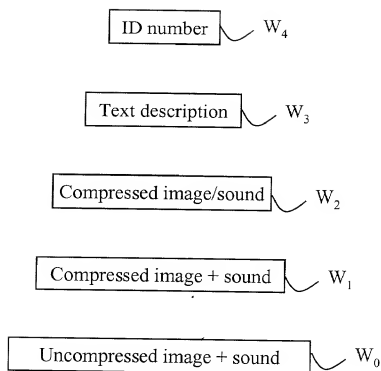
*Figure 2*

**Figure 3**

*Figure 4*

**Figure 5****Figure 6a**

*Figure 6b*

*Figure 6c*

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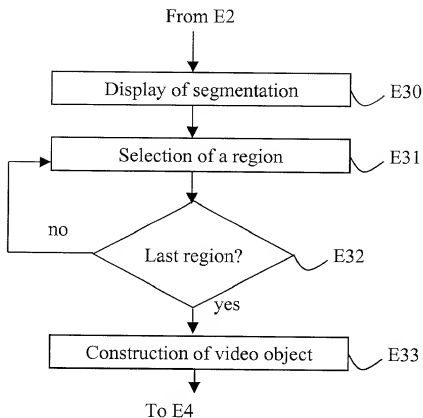


Figure 7

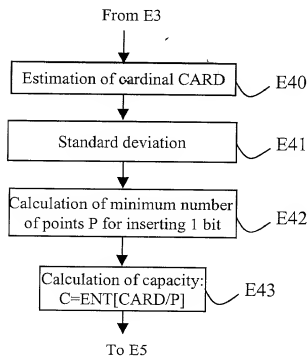
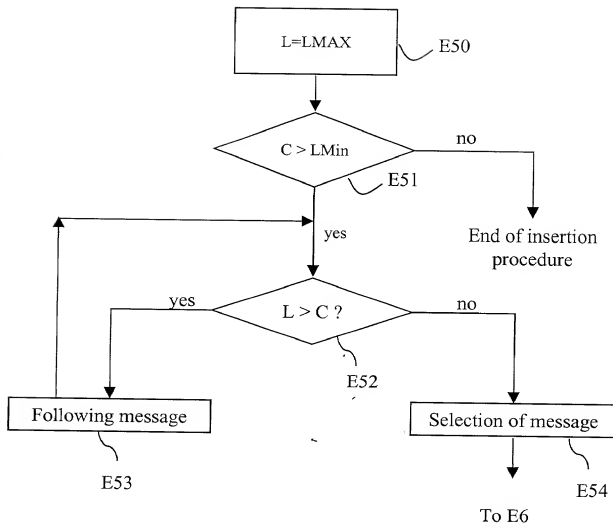
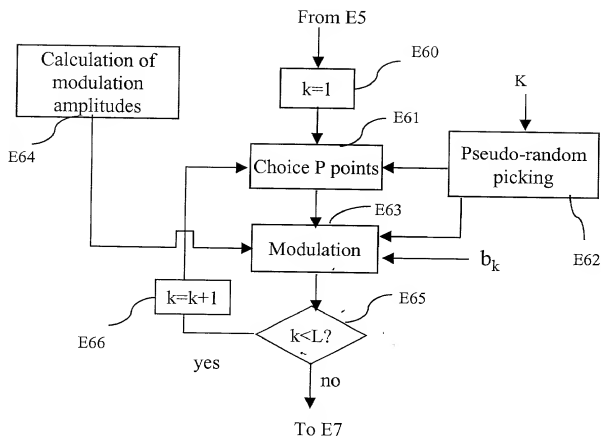


Figure 8

**Figure 9**

**Figure 10**

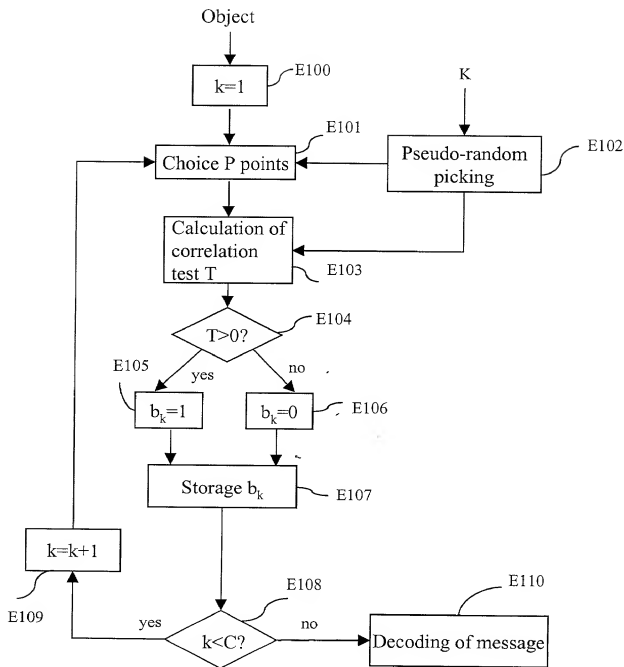


Figure 11

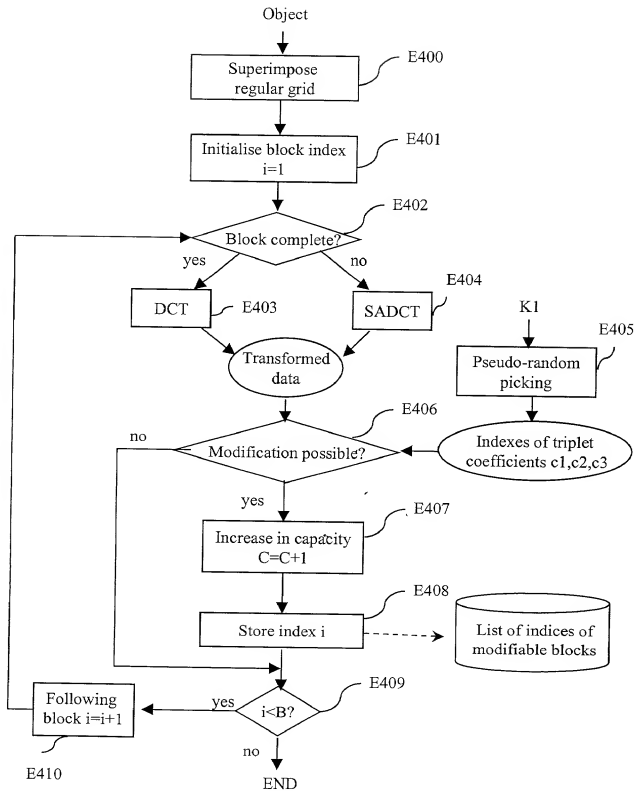


Figure 12

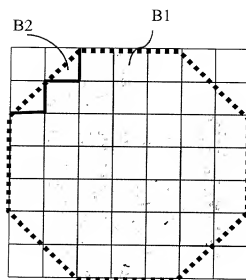


Figure 13

		1	2						
	3	4	5						
6	7	8							

Figure 14

C1	C2	C3	BIT
H	M	L	1
M	H	L	
H	H	L	
M	L	H	0
L	M	H	
L	L	H	
H	L	M	NEUTRAL
L	H	M	
M	M	M	

Figure 15

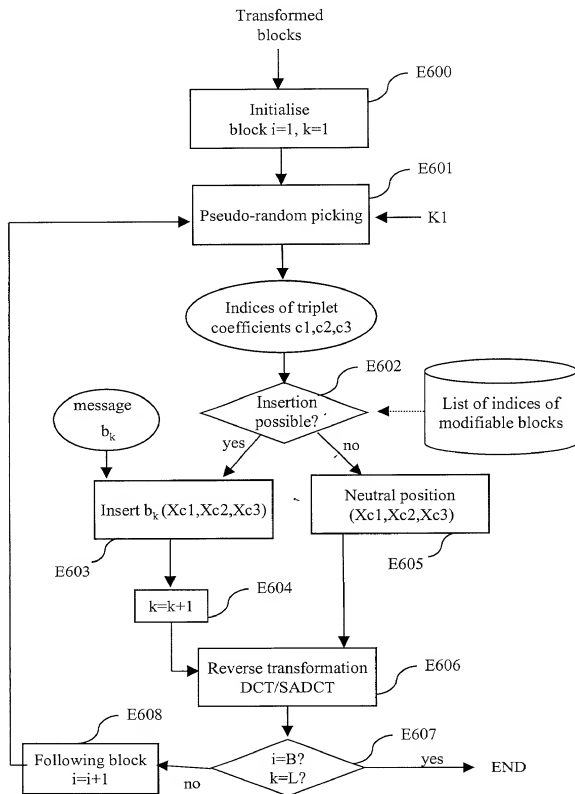


Figure 16

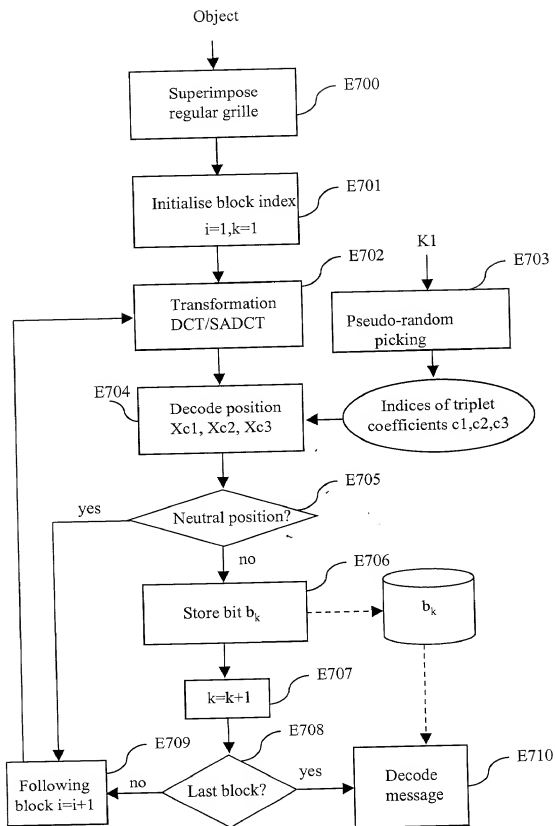


Figure 17